



IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re the Application of: **OHASHI, Hitoshi et al.**

Group Art Unit: 1792

Serial No.: 10/047,992

Examiner: **TALBOT, Brian K.**

Filed: **January 17, 2002**

P.T.O. Confirmation No.: 5363

For. **METHOD AND APPARATUS FOR FORMING ELECTRIC CIRCUIT FIXING THE SEMICONDUCTOR DEVICE USING SUBSTRATE JIG**

AMENDMENT ACCOMPANYING A REQUEST
FOR CONTINUED EXAMINATION (RCE)

Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Date: April 16, 2009

Sir:

In response to the Examiner's Answer dated **February 24, 2009**, entry of the following amendments and remarks, further examination, and reconsideration are respectfully requested.

Amendments to the Claims are reflected in the listing of claims which begins on page 2 of this paper.

Remarks/Arguments begin on page 8 of this paper.